

Ti Ipc User Guide

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IPC Users Guide. Inter-Processor Communication (IPC) provides a processor-agnostic API which can be used for communication between processors in a multi-processor environment (inter-core), communication to other threads on same processor (inter-process), and communication to peripherals (inter-device). The API supports message passing, streams, and linked lists.

IPC-Users-Guide-Texas-Instruments-Wiki

IPC is a component containing packages that are designed to allow communication between processors in a multi-processor environment and communication to peripherals. This communication includes message passing, streams, and linked lists. These work transparently in both uni-processor and multi- processor configurations.

SYS/BIOS-Inter-Processor-Communication-(IPC)-1.25-(Rev-E)

The IPC product contains an examples/archive directory with device-specific examples. Once identifying your device, the examples can be unzipped anywhere on your build host. Typically once unzipped, the user edits the example's individual products.mak file and simply invokes make.

IPC-Users-Guide/Examples-Texas-Instruments-Wiki

IPC can be used to communicats with the following: other threads on the same processor. threads on other processors running SYS/BIOS. threads on GPP processors running a High-Level Operating System (HLOS) IPC was designed with the needs of a wide variety of users in mind.

IPC-Users-Guide/About-IPC-Texas-Instruments-Wiki

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MCUSW-Cdd-Ipc-User-Guide-Texas-Instruments

The ti.sdo.ipc.Notify module manages the multiplexing/demultiplexing of software interrupts over hardware interrupts. In order to use any Notify APIs, you must first call ipc_start(). This sets up all the necessary Notify drivers, shared memory, and interprocessor interrupts.

IPC-Users-Guide/Notify-Module-Texas-Instruments-Wiki

IPC provides an implementation of the IMessageQTransport interface called ti.sdo.ipc.transports.TransportShm (shared memory). You can write other implementations to meet your needs. When a transport is created via a transport-specific create() call, a remote processor ID (defined via the MultiProc module) is specified.

IPC-Users-Guide/MessageQ-Module-Texas-Instruments-Wiki

Overview. IPC is a generic term of Inter-Processor Communication referred widely in the industry, but also a package in TI Processor SDK for multi-core communication. In generic usage, there are different ways for multi-core communication such as OpenCL, DCE, TI-IPC, and etc. In TI 's IPC package, it uses a set of modules to facilitate the inter-processor communication.

3.7-IPC-Processor-SDK-Linux-Documentation

Download Ebook Ti Ipc User Guide Ti Ipc User Guide This wiki-based user's guide is the new home of the Inter-Processor Communication (IPC) User's Guide (SPRUGO6). That PDF-based document will no longer be updated. This user's guide contains the topics in the following list. It also links to API

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IPC Overview. The IPC product provides software connectivity between multiple processors. Each processor may run either an HLOS (e.g. Linux, QNX) or an RTOS (e.g. SYS/BIOS). IPC is an open source project, currently managed with git, and maintained at http://git.ti.com/ipc/icodev . Full source browsing, including all changes, are available here .

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