

Vtu Hdl Lab Viva Questions And Answers

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Vtu-Lab-Viva-Questions-Hardware-Description-Language-1
VHDL Programming Lab viva questions and answers 1. VHDL means. Very high speed integrated circuit hardware description language. 2. List the types of HDL? VHDL and Verilog HDL . 3. Software used in VHDL lab is? Xilinx 4. Name any two programming tools of VHDL? FPGA, CPLD and ASIC 5. FPGA means. Field programmable gate array. 6. CPLD means.

VHDL-Programming-Lab-viva-questions-and-answers-pdf-VHDL-1
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3. Write HDL code to control speed, direction of DC and Stepper motor. 4. Write HDL code to accept Analog signal, Temperature sensor and display the data on LCD or Seven segment display. 5. Write HDL code to generate different waveforms (Sine, Square, Triangle, Ramp etc..) using DAC - change the frequency. 6. Write HDL code to simulate Elevator ...

HDL-Lab-syllabus-for-EC-5-Sem-2017-scheme-VTU-CBCS-1
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• Verilog HDL is a general-purpose hardware description language that is easy to learn and easy to use. It is similar in syntax to the C programming language. Designers with C programming experience will find it easy to learn Verilog HDL. • Verilog HDL allows different levels of abstraction to be mixed in the same model. Thus, a

Subject-Code-18EC57-LA-Marks-20-04-Exam-Marks-80-Hours-1
VHDL Lab Manual Department of E & C, SSIT, Tumkur. Page 4 When the table is complete, your project properties should look like the following: 7. Click Next to proceed to the Create New Source window in the New Project Wizard. At the end of the next section, your new project will be created. Creating an HDL Source

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